

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
)
Kenichi KADOTA)
) Group Art Unit: Not yet assigned
Application No.: Not Yet Assigned)
) Examiner: Not yet assigned
Filed: February 24, 2004)
)
For: ANALYSIS METHOD FOR)
SEMICONDUCTOR DEVICE,)
ANALYSIS SYSTEM AND A)
COMPUTER PROGRAM)
PRODUCT)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(b)

Pursuant to 37 C.F.R. §§1.56 and 1.97(b), applicant brings to the Examiner's attention the documents listed on attached Form PTO-1449. Copies of the listed documents are attached. Applicant respectfully requests that the Examiner consider the documents listed on attached Form PTO-1449 and indicate that they were considered by making an appropriate notation on this form.

This Information Disclosure Statement is being filed with the above-referenced application.

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The following are listed on the accompanying PTO-1449 and are in a non-English language. English-language abstract are attached.

1. Japanese Patent Application Laid-Open Publication No. 2001-110867 -

This document discloses a quality control system analyzing a relationship between yield deterioration and a process parameter. Also, the relevance of this document is discussed at page 3 of the specification of the present application.

2. Japanese Patent Application Laid-Open Publication No. 2003-37143 -

This document discloses systematic yield loss areas on a wafer are extracted automatically.

This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that each or all of the listed documents are material or constitute "prior art." If the Examiner applies any of the documents as prior art against any claim in the application and applicant determine that the cited documents do not constitute "prior art" under United States law, applicant reserves the right to present to the Office the relevant facts and law regarding the appropriate status of such documents. Applicant further reserves the right to take appropriate action to establish the patentability of the disclosed invention over the listed documents, should one or more of the documents be applied against the claims of the present application.

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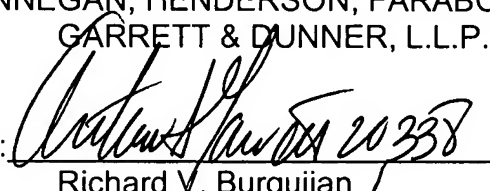
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If there is any fee due in connection with the filing of this Statement, please charge the fee to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,
GARRETT & DUNNER, L.L.P.

Dated: February 24, 2004

By:  20338
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Enclosures
RVB/FPD/cma

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INFORMATION DISCLOSURE CITATION

Atty. Docket No.	03180.0354	Application No.	TBA
Applicant	Kenichi KADOTA		
Filing Date	February 24, 2004	Group:	TBA

U.S. PATENT DOCUMENTS							
Examiner Initial*		Document Number	Issue Date	Name	Class	Sub Class	Filing Date If Appropriate

FOREIGN PATENT DOCUMENTS							
		Document Number	Publication Date	Country	Class	Sub Class	Translation Yes or No
		2001-110867	04/20/2001	Japan			Abstract
		2003-037143	02/07/2003	Japan			Abstract

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)	

Examiner	Date Considered
<p>*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>	
Form PTO 1449	Patent and Trademark Office - U.S. Department of Commerce